

FORM PTO-1449 U.S. Department of Commerce
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566.30812VC2

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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APPLICANT
H. OGAWA et al.

FILING DATE
Even Date Herewith

GROUP 265/978
3729 (Anticipated)




U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
													YES	NO
100	5	9-	1	4	4	1	5	5	08/84	Japan	-----	-----	Abstract	
	6	0-	3	8	4	8	5	U	03/85	Japan	-----	-----		X
	6	0-	4	6	6	8	0	U	04/85	Japan	-----	-----		X
	6	0-	5	5	0	8	9	U	04/85	Japan	-----	-----		X
	6	0-	1	3	3	7	4	4	07/85	Japan	-----	-----	Abstract	
100 60	-	1	5	9	5	9	6	U	10/85	Japan	-----	-----		X

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		<p>D. Smith, "The M212--A Zero-Glue Single-Chip VLSI Winchester Controller with On-Board 10 MIPS Processor", <u>Wescon/86 Conference Record</u>, pp. 26/5/1-5, paper presented at Wescon/86, Anaheim, CA, November 18-20, 1986 (abstract only, INSPEC Abstract Number: B88000805, C88002181).</p>

EXAMINER

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09/01/25
 03/21/01

U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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